

TimeTagger4-2G-PCle



The TimeTagger4-2G-PCle is a high bandwidth, low cost common start time-to-digital converter (TDC). The timestamps of leading or trailing edges of digital pulses are recorded with a single shot time resolution of 500 ps.

Facts

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| Channels | 1 AC-coupled start channel 4 AC-coupled stop channels |
| Dead-time btw. hits on same channel | 1 ns |
| Default range | 8,388 μ s |
| Max. start frequency | 250 Mhz |
| L0 FIFO | 1000 words/channel |
| L2 FIFO | 1000 words |
| TDC binsize | 500 ps |
| TDC timing resolution | 500 ps |
| Connectors | 5x LEMO 00 |
| Throughput | 200 Mbyte/s |
| Form factor | PCIe 1.1 x1, bus master |